* mass storage is 256 bytes
* RAM is 64 bytes total
* Code is 32 bytes
* Data is 16 bytes
* Stack is 16 bytes
* the design would be around 19ft long and 9ft high
* CS + IP = MAR, would be used to address all the data in mass storage
* Has no visible queue, could be added if desired
* Extra segment removed
* 2 memory related registers removed
* ram is still segmented into code, data, and ram
* Base addresses for code data and ram are still kept
* AX, BX, CX, DX are kept for ease of calculations
* Flags not shown but would be included in the alu as a display
* ALU kept the same
* Input and output are consolidated for ease of operation, and wiring
* All memory dimensions are heavily subject to change